## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 7,032,013 B2 Page 1 of 1

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INVENTOR(S): Julie Ann Ward et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 9, line 12, in Claim 9, after "n+1 ports" insert -- of the node --.

In column 9, line 13, in Claim 9, delete "ports." and insert -- ports of the node. --, therefor.

In column 9, line 16, in Claim 10, delete "port." and insert -- port prior to the partitioning so that a plurality of ports of the port-coupled interconnect device are associated with the node. --, therefor.

In column 10, line 26, in Claim 21, after "wherein" insert -- a --.

Signed and Sealed this

Thirteenth Day of April, 2010

David J. Kappos Director of the United States Patent and Trademark Office